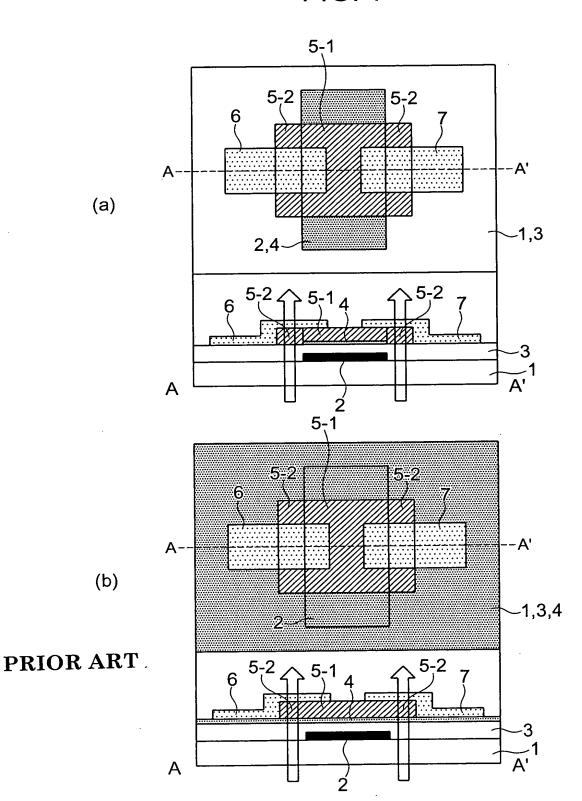


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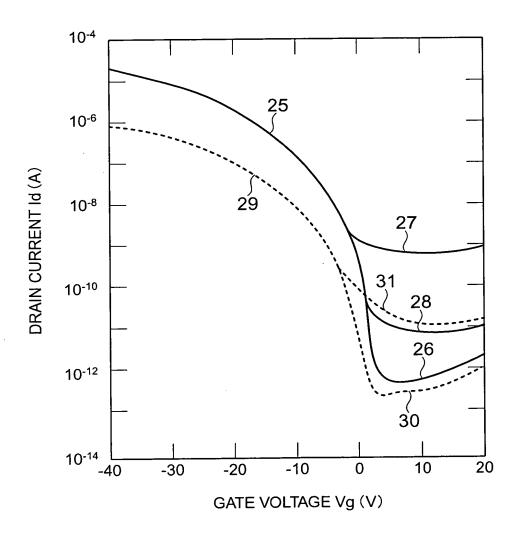
FIG. 1



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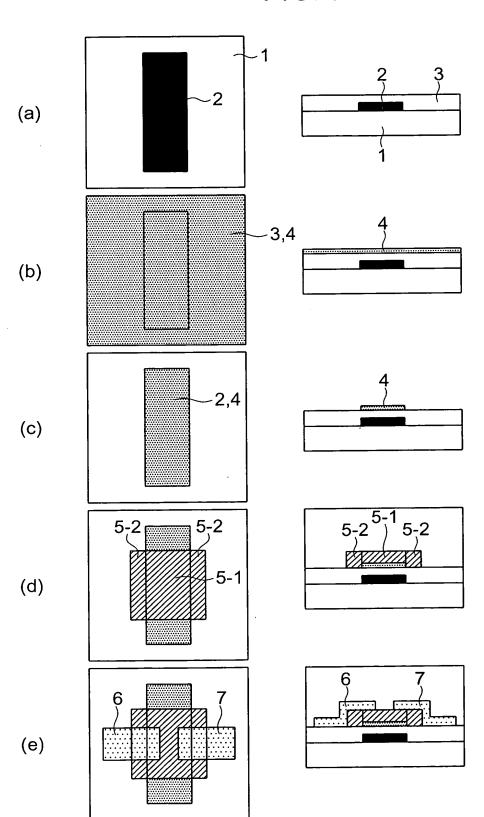
FIG. 2



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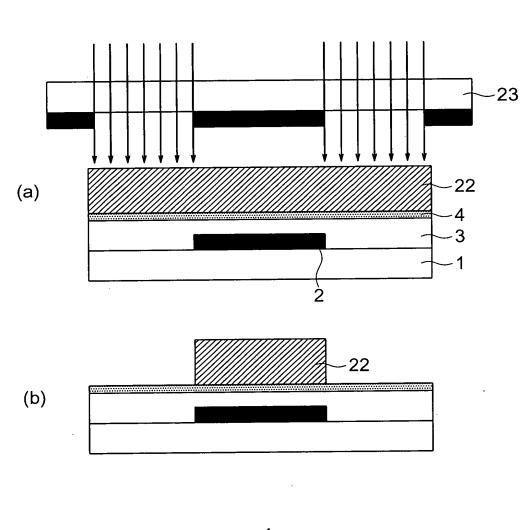
FIG. 3

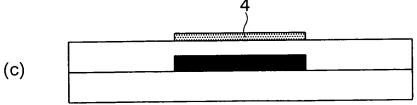


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FIG. 4

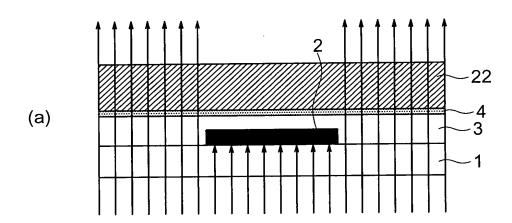


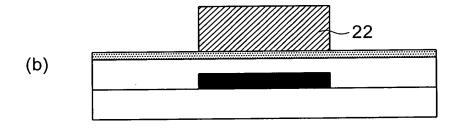


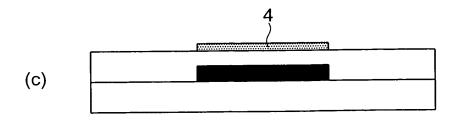
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FIG. 5



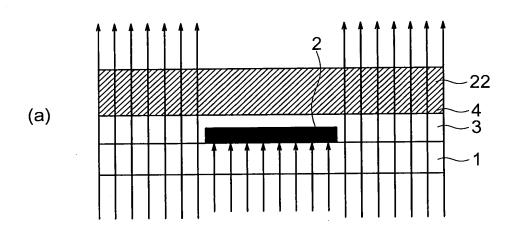


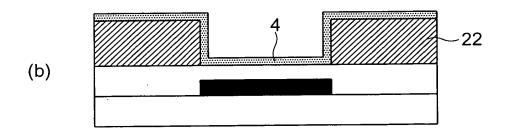


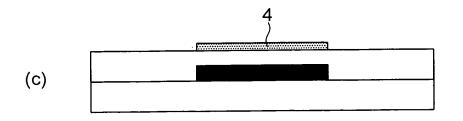
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Title: Thin Film Transistor ....
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FIG. 6



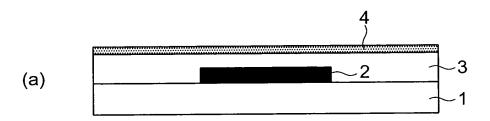


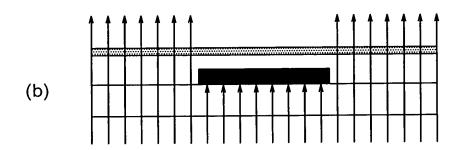


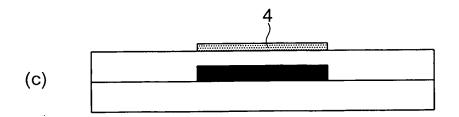
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FIG. 7



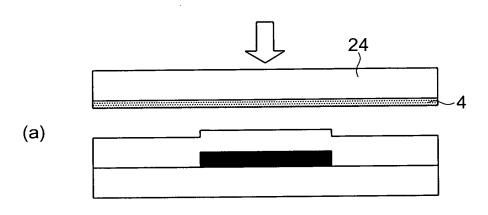


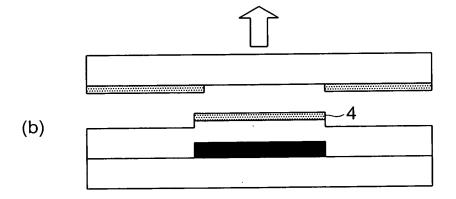


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FIG. 8

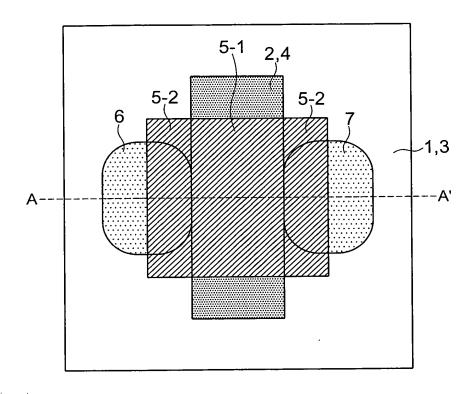


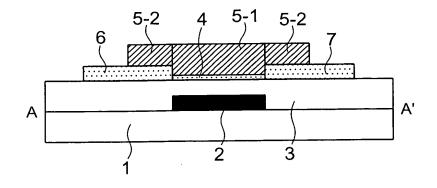


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Title: Thin Film Transistor ....
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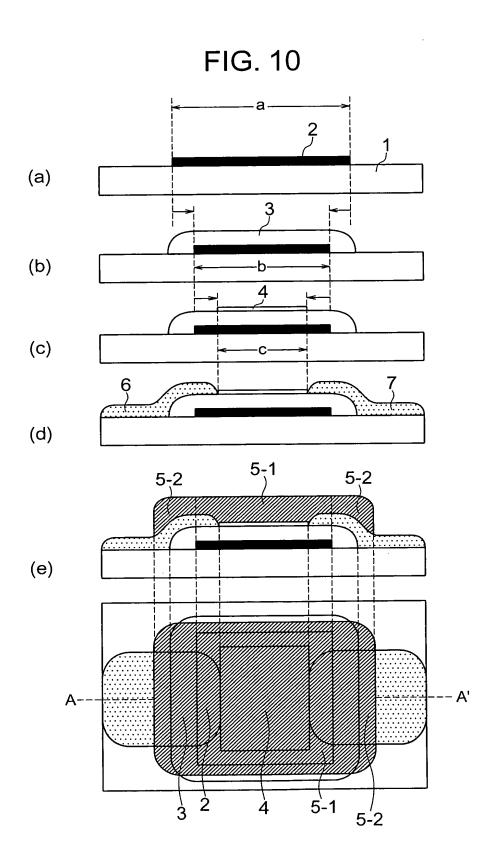
FIG. 9





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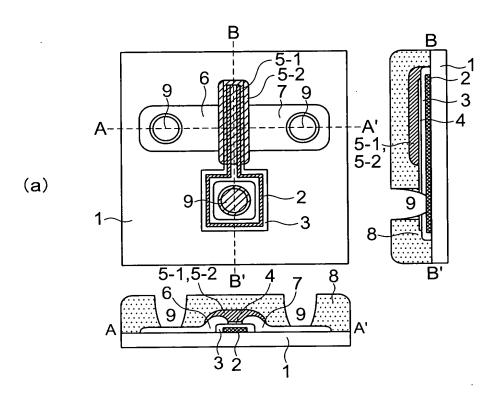
Title: Thin Film Transistor ....
14 sheets of drawings; sheet 10 of 14

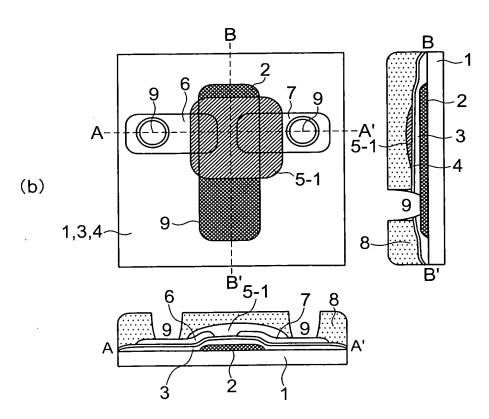


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Title: Thin Film Transistor ....
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FIG. 11

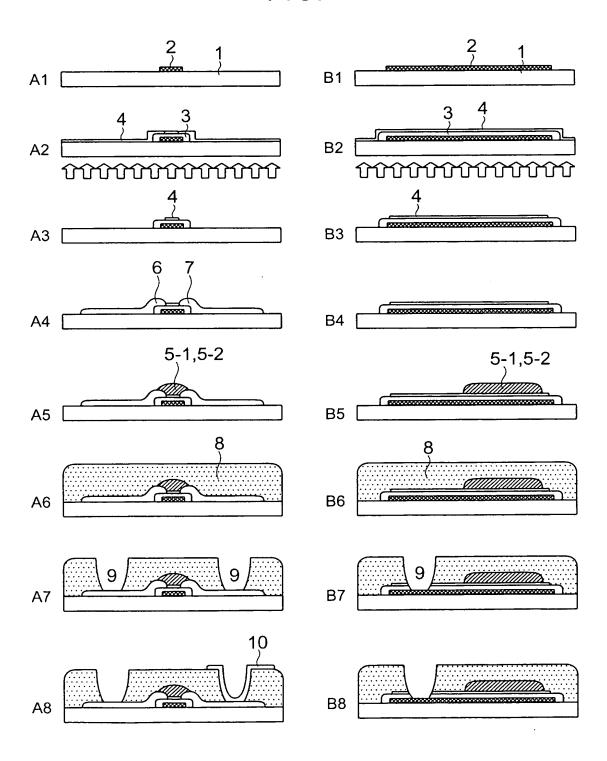




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Title: Thin Film Transistor ....
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FIG. 12

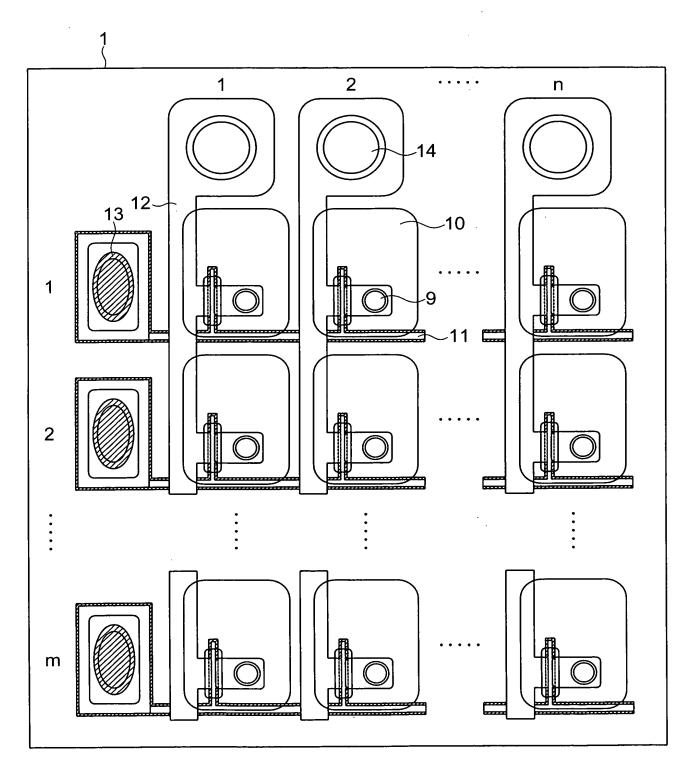


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FIG. 13



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FIG. 14

